Parallel *k*-Core Decomposition: Theory and Practice

Youzhe Liu, Xiaojun Dong, Yan Gu, Yihan Sun

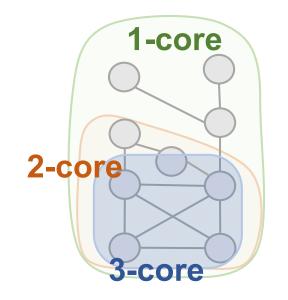
University of California, Riverside



How to Define Dense Subgraphs (formally)?

• *k*-core definition:

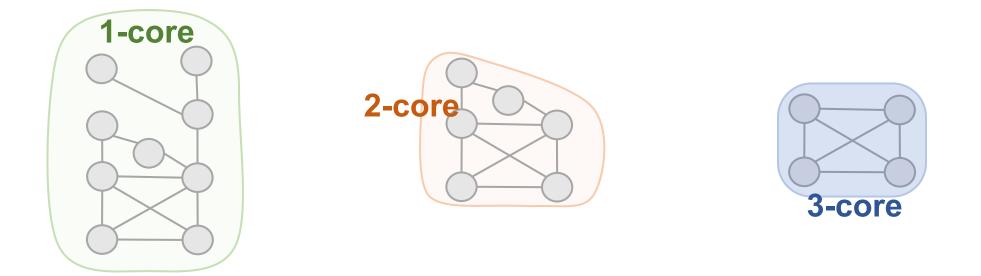
Given a graph *G*, *k*-core is the **subgraph** after **removing all** the vertices with degrees smaller than *k*



How to Define Dense Subgraphs (formally)?

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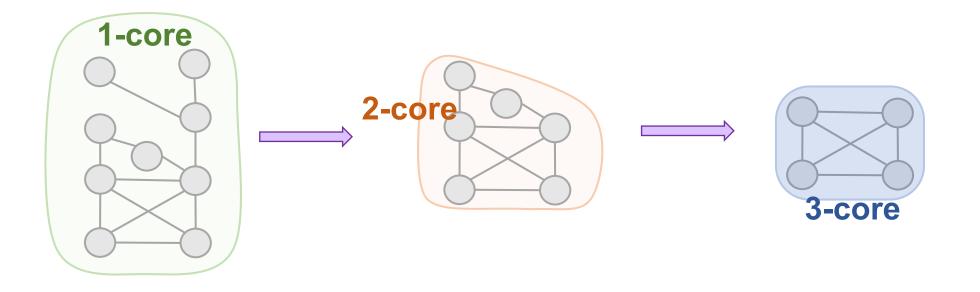
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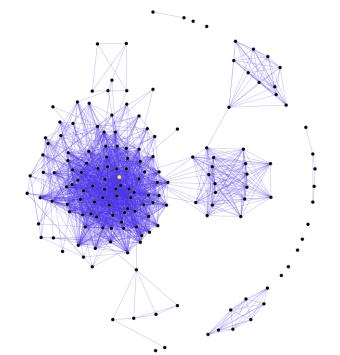
How to Define Dense Subgraphs (formally)?

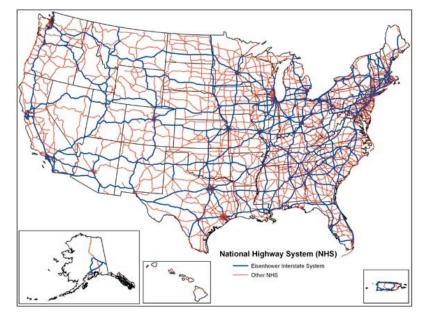
k-core decomposition:

The process of listing all the k-core structures in the graph *G*



k-core is widely used



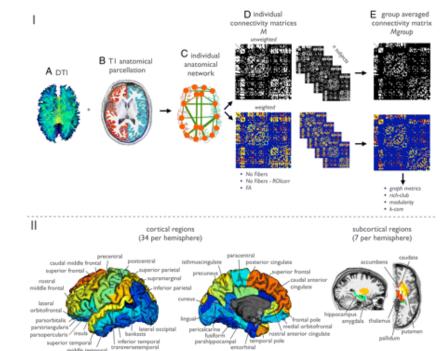


Community detection [1,2,3] Infra-network robustness [4]

[1] Marián Boguná, Romualdo Pastor-Satorras, Albert Dí az Guilera, and Alex Arenas. 2004. Models of social networks based on social distance attachment. Physical Review E—Statistical, Nonlinear, and Soft Matter Physics 70, 5 (2004)
 [2] Christos Giatsidis, Dimitrios M Thilikos, and Michalis Vazirgiannis. 2011. Evaluating cooperation in communities with the k-core structure. In 2011 International conference on advances in social networks analysis and mining.
 [3] Maksim Kitsak, Lazaros K Gallos, Shlomo Havlin, Fredrik Liljeros, Lev Muchnik, H Eugene Stanley, and Herná n A Makse. 2010. Identification of influential spreaders in complex networks. Nature physics 6, 11 (2010), 888–893.
 [4] Kate Burleson-Lesser, Flaviano Morone, Maria S Tomassone, and Herná n A Makse. 2020. K-core robustness in ecological and financial networks. Scientific reports 10, 1 (2020), 3357.

[5] Yizong Cheng, Chen Lu, and Nan Wang. 2013. Local k-core clustering for gene networks. In 2013 IEEE International Conference on Bioinformatics and Biomedicine. IEEE, 9–15.

[6] Arnold I Emerson, Simeon Andrews, Ikhlak Ahmed, Thasni KA Azis, and Joel A Malek. 2015. K-core decomposition of a protein domain co-occurrence network reveals lower cancer mutation rates for interior cores. Journal of clinical bioinformatics 5 (2015), 1-11.



Strongly cohesive structure in biology [5,6]

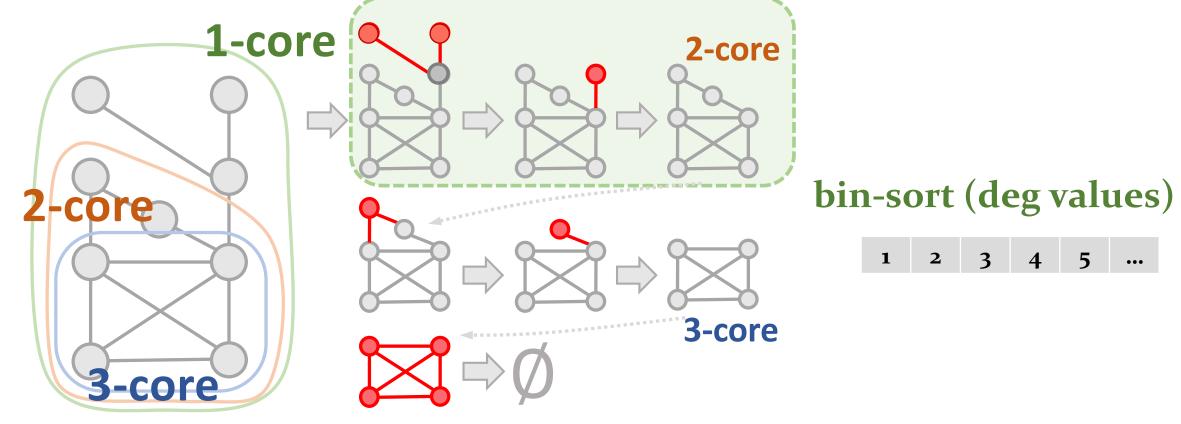
k-core is a subroutine for many problems

- Dense subgraphs discovery [1]
- Hierarchical graph clustering [2]
- Graph degeneracy for graph learning [3]
- Graph coloring [4]
- •

[1] Ahmet Erdem Sariyüce, C. Seshadhri, and Ali Pinar. 2018. Local algorithms for hierarchical dense subgraph discovery. Proc. VLDB Endow. 12, 1 (September 2018), 43–56. https://doi.org/10.14778/3275536.3275540
 [2] Christos Giatsidis, Fragkiskos D. Malliaros, Dimitrios M. Thilikos, and Michalis Vazirgiannis. 2014. CORECLUSTER: a degeneracy based graph clustering framework. In Proceedings of the Twenty-Eighth AAAI Conference on Artificial Intelligence (AAAI'14). AAAI Press, 44–50.
 [3] Guillaume Salha, Romain Hennequin, Viet Anh Tran, and Michalis Vazirgiannis. 2019. A degeneracy framework for scalable graph autoencoders. In Proceedings of the 28th International Joint Conference on Artificial Intelligence (IJCAI'19).
 [4] Suman K. Bera, Amit Chakrabarti, and Prantar Ghosh. Graph Coloring via Degeneracy in Streaming and Other Space-Conscious Models. In 47th International Colloquium on Automata, Languages, and Programming (ICALP 2020). Leibniz International Proceedings in Informatics (LIPIc

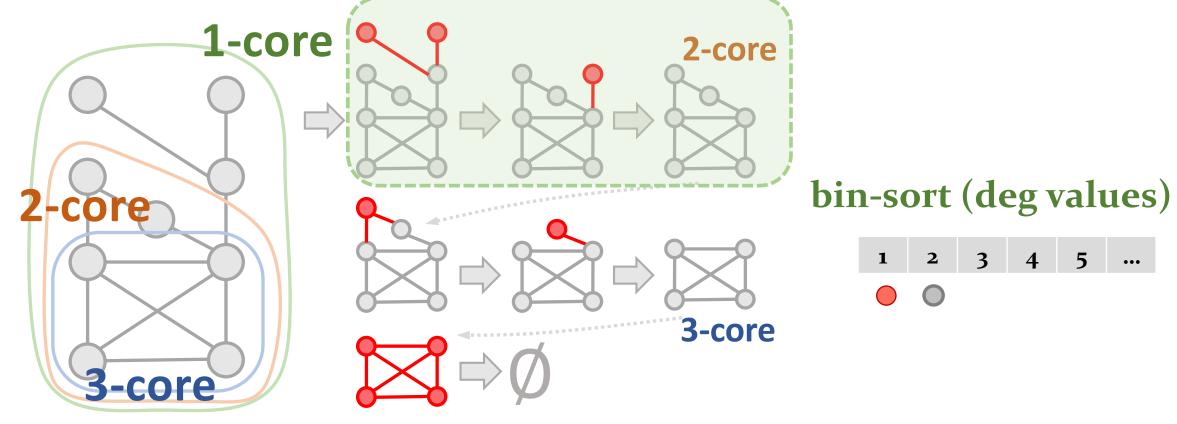
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• Efficient sequential algorithm [BZ03]: follows the definition



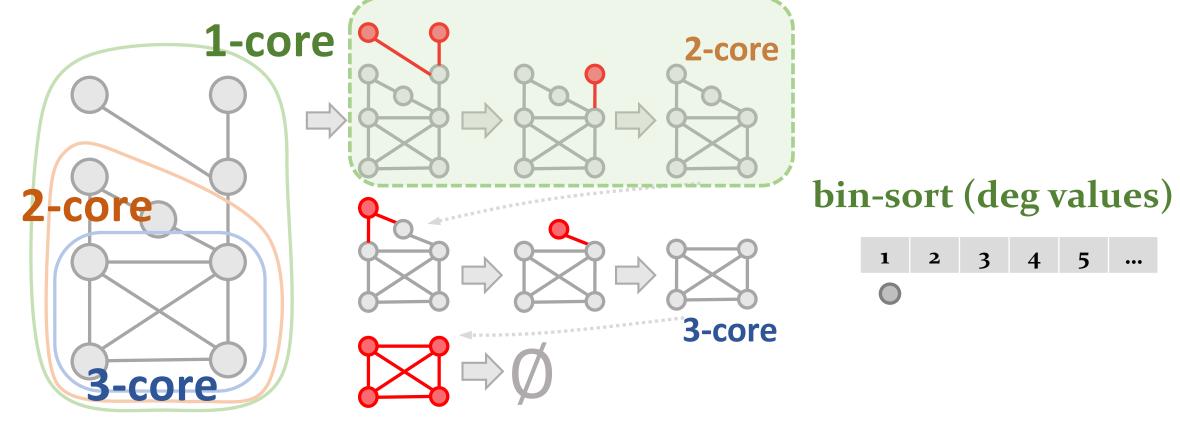
7

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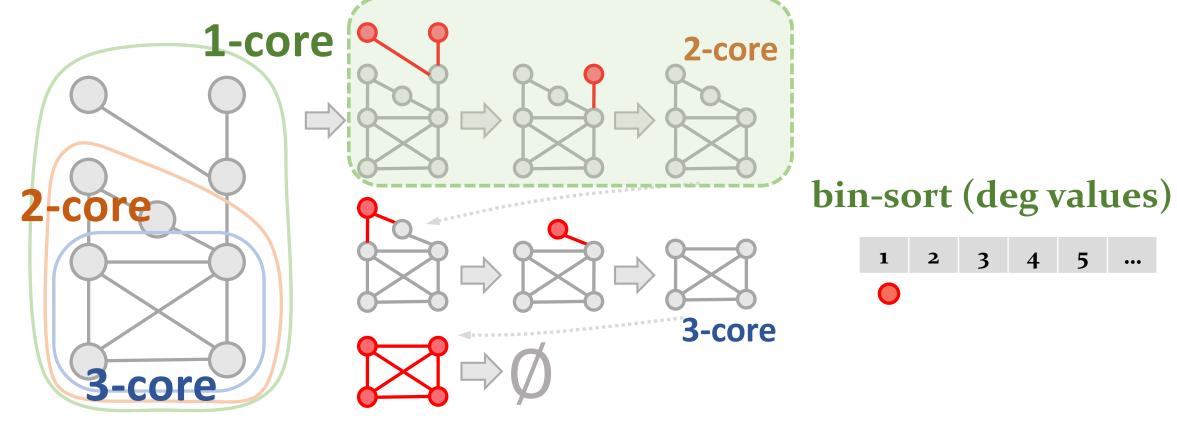


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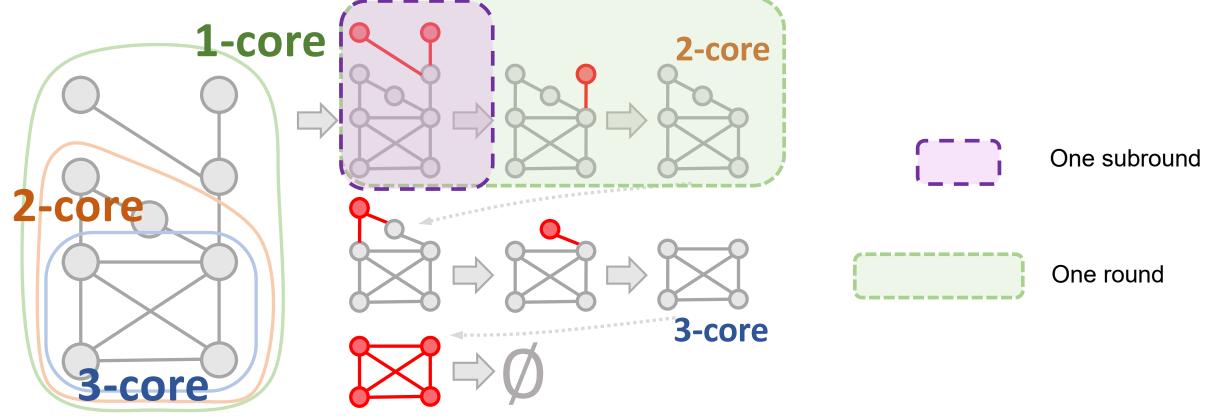
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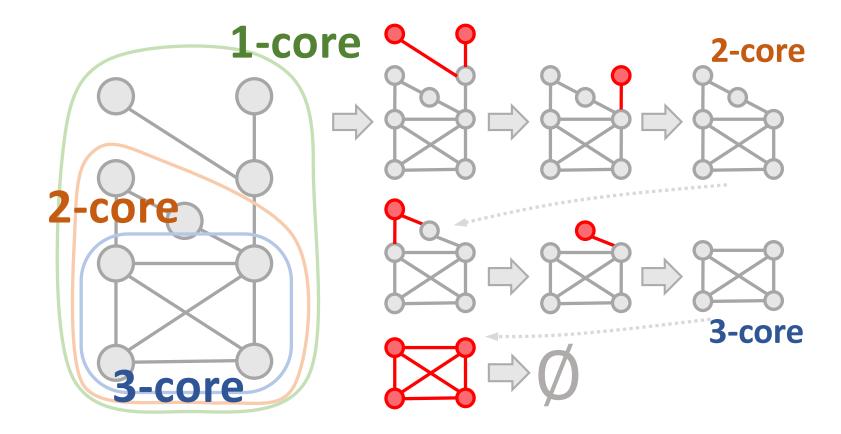


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O(|V| + |E|)

The sizes of real-world graphs are very large

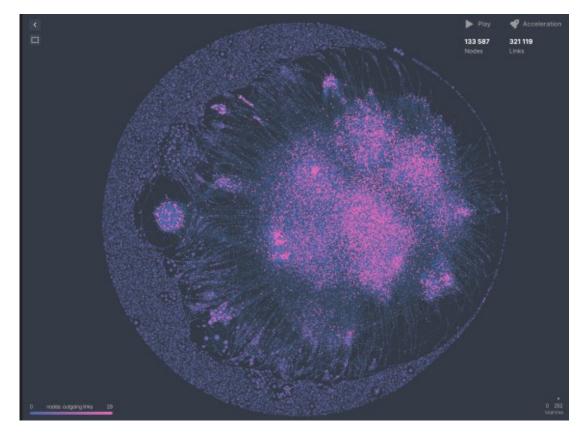


Figure source: https://nightingaledvs.com/how-to-visualize-a-graph-with-a-million-nodes/

Web graphs [1]: **Billions** of edges

[1] Robert Meusel, Oliver Lehmberg, Christian Bizer, and Sebastiano Vigna. 2014. Web Data Commons — Hyperlink Graphs. <u>http://webdatacommons.org/</u> hyperlinkgraph

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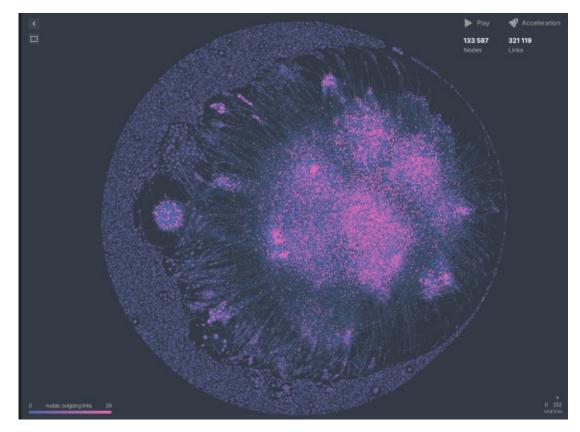


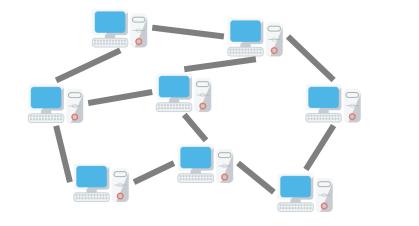
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Web graphs [1]: **Billions** of edges

Performance is important for *k*-core decomposition!

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Multi-core Parallelism





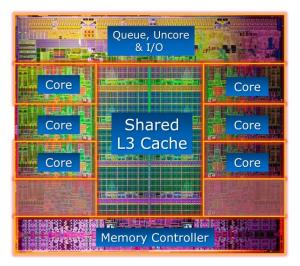
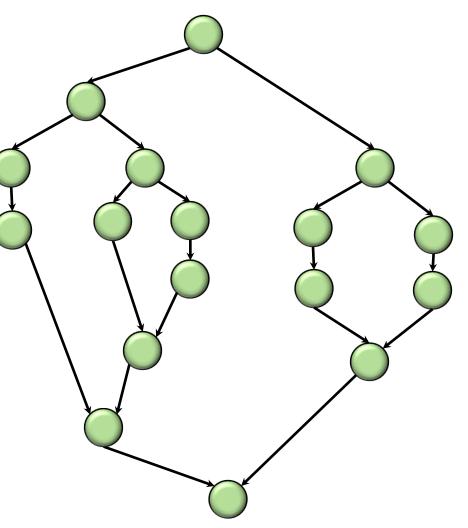


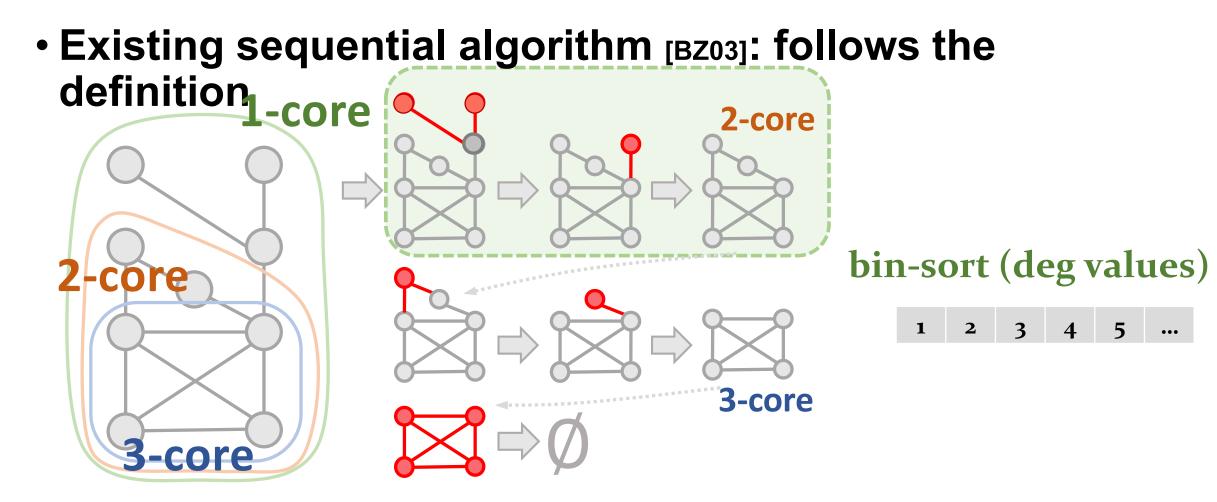
Figure credit: Wikipedia

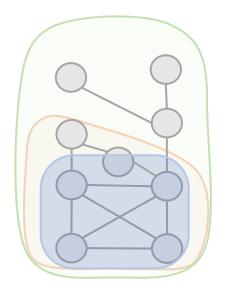
Parallelism is ubiquitous and can be used to accelerate algorithms

Parallel Computational Model [1]

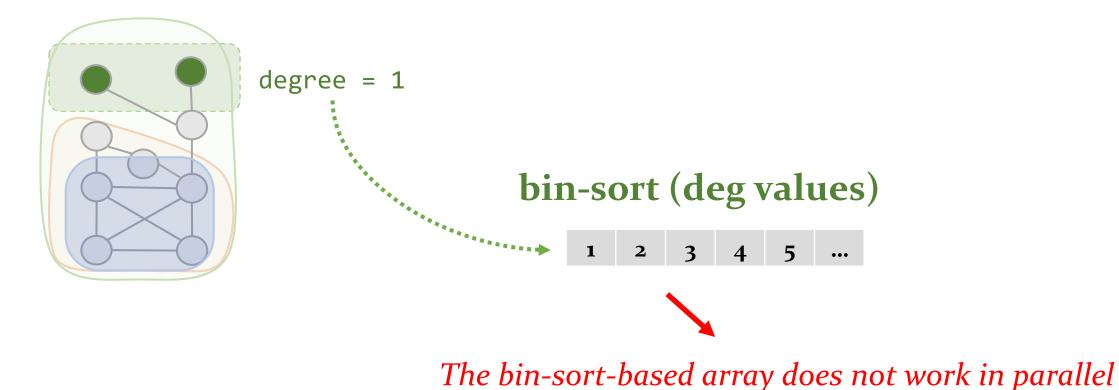
- Shared-memory multi-core setting
- Work: the total number of operations
 - = running time on one core
- Work-efficient: The Work matches the complexity of the best sequential algorithm
- Work-efficient is a primary goal for parallel algorithm design

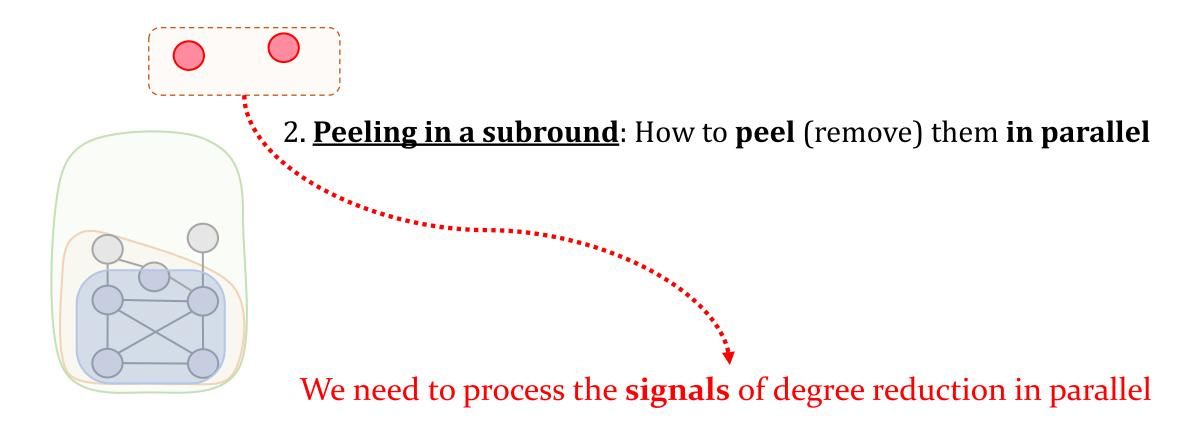






1. <u>Framework</u>: How to **arrange & gather** the vertices with target degree values





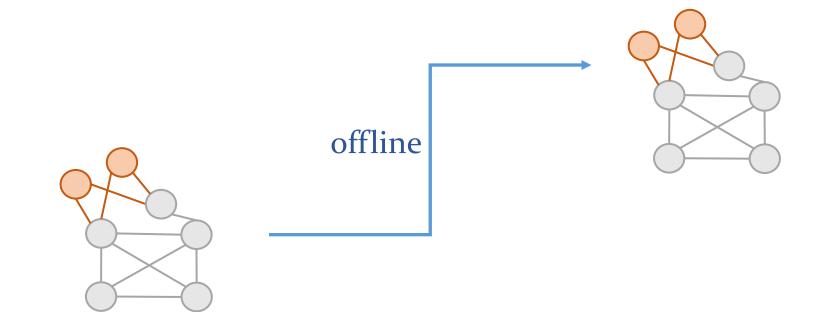
There are some existing parallel solutions

Different parallel peeling strategies

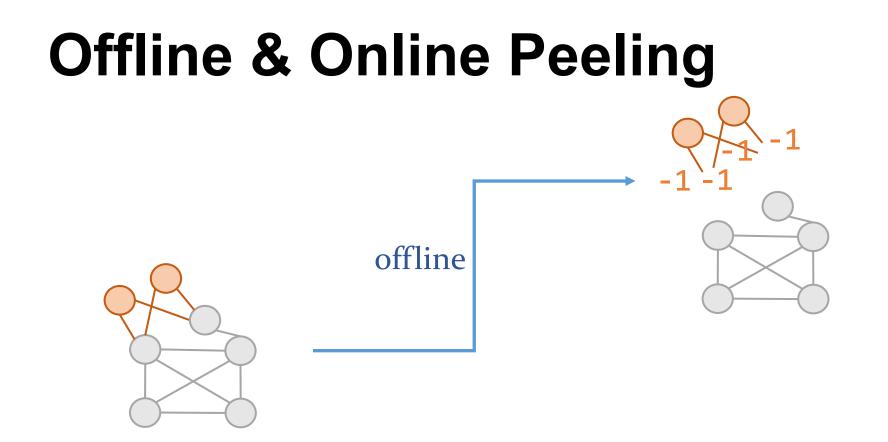
• Offline-peeling: Julienne^[1]

• Online-peeling: ParK_[2], PKC_[3]

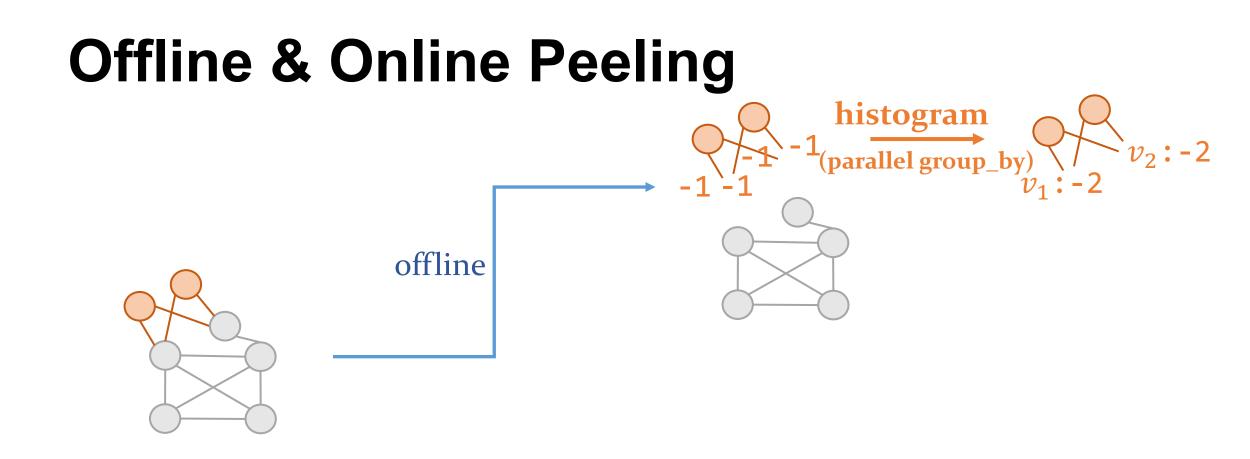
Laxman Dhulipala, Guy Blelloch, and Julian Shun. Julienne: A Framework for Parallel Graph Algorithms using Work-efficient Bucketing, SPAA '17
 Dasari, Naga Shailaja et al. "ParK: An efficient algorithm for k-core decomposition on multicore processors." *Big Data*, 2014
 H. Kabir and K. Madduri, "Parallel k-Core Decomposition on Multicore Platforms," *IPDPSW*, 2017



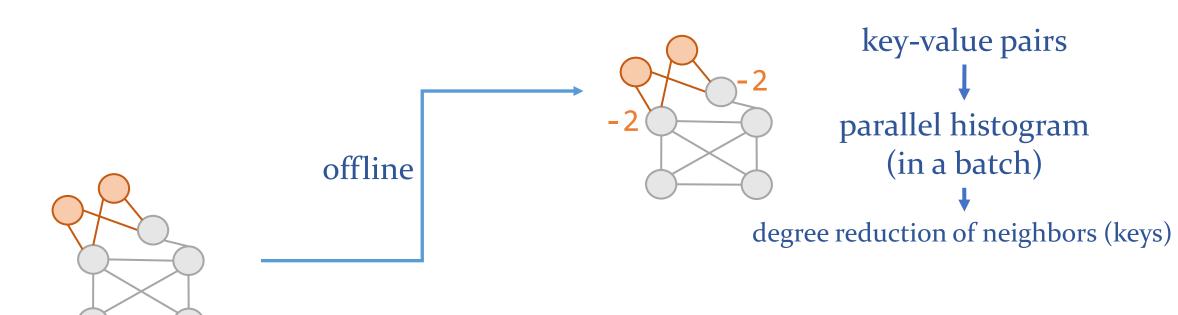
decompose to 3-core ↓ peel vertices with degree <3



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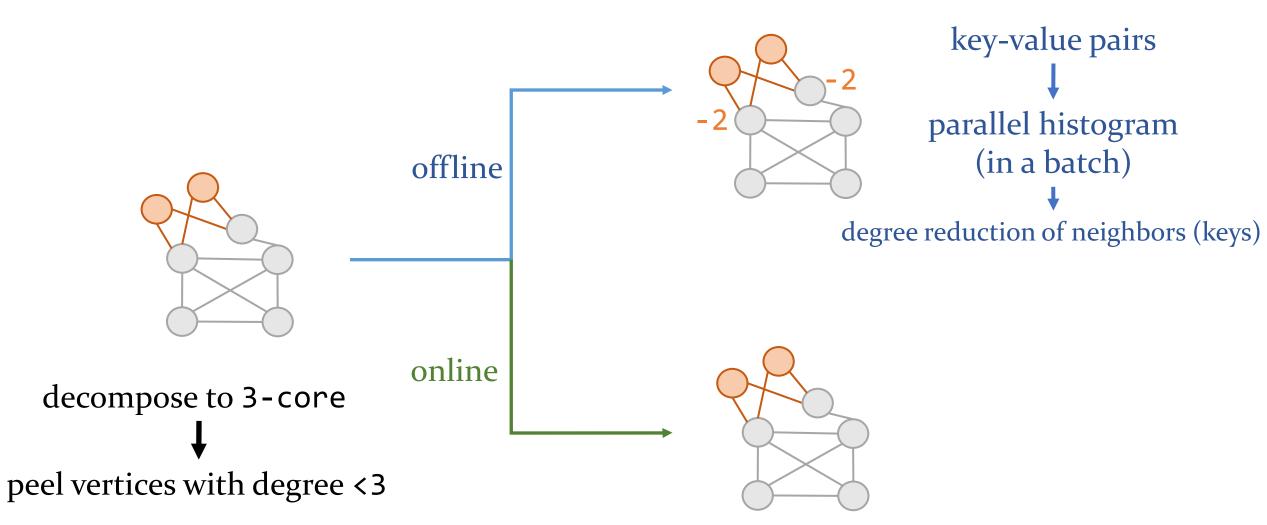


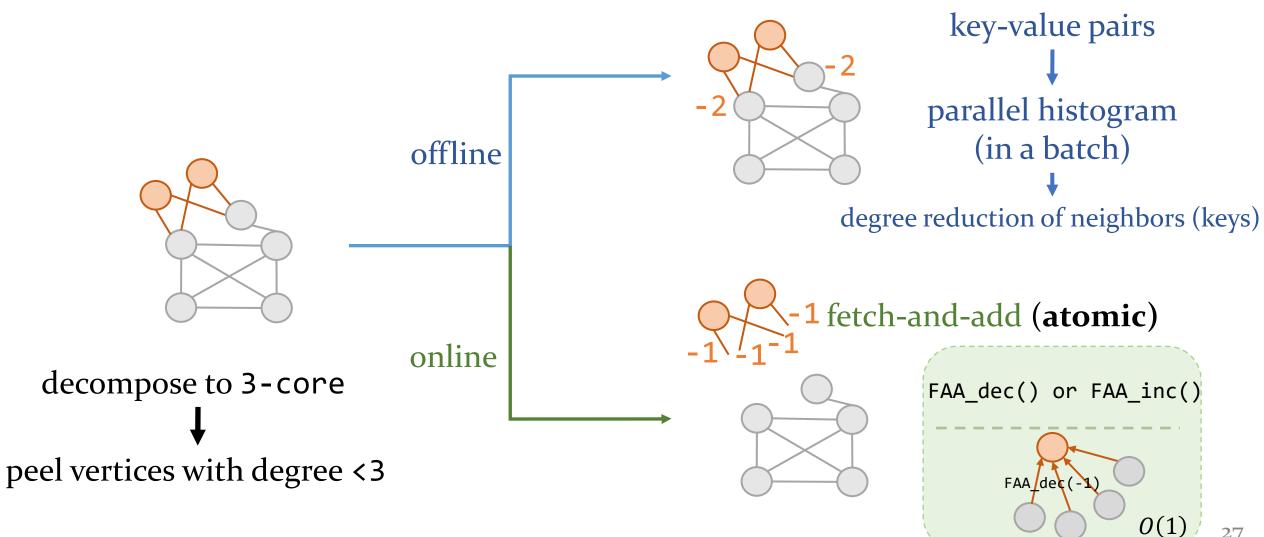
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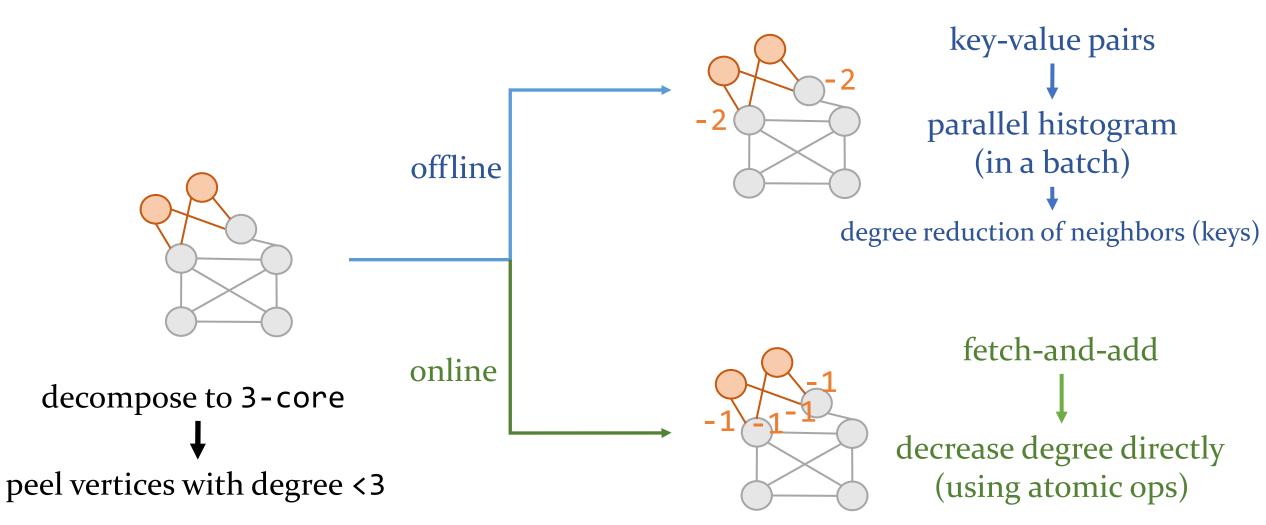


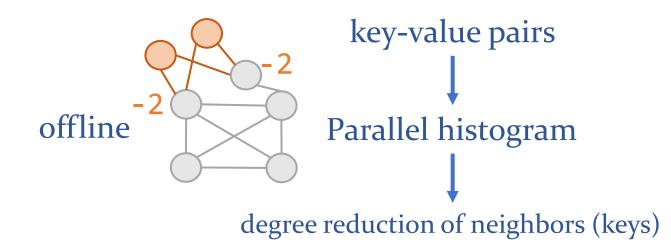
peel vertices with degree <3

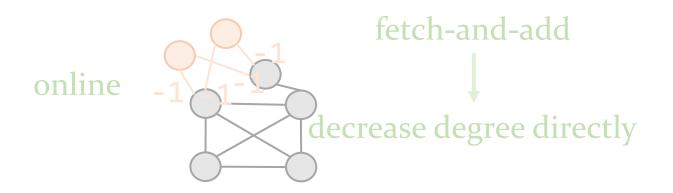
decompose to 3-core



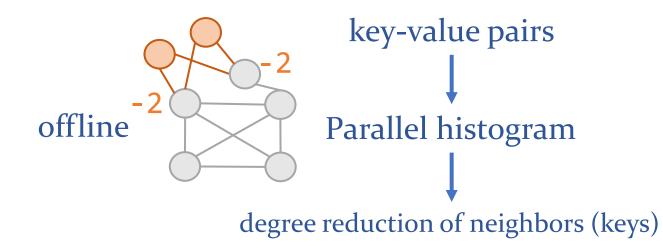


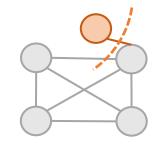




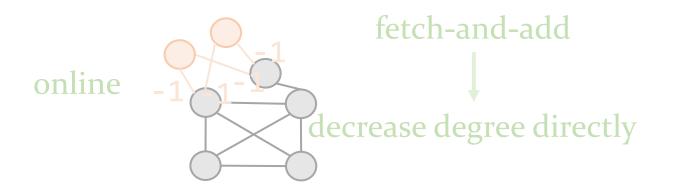


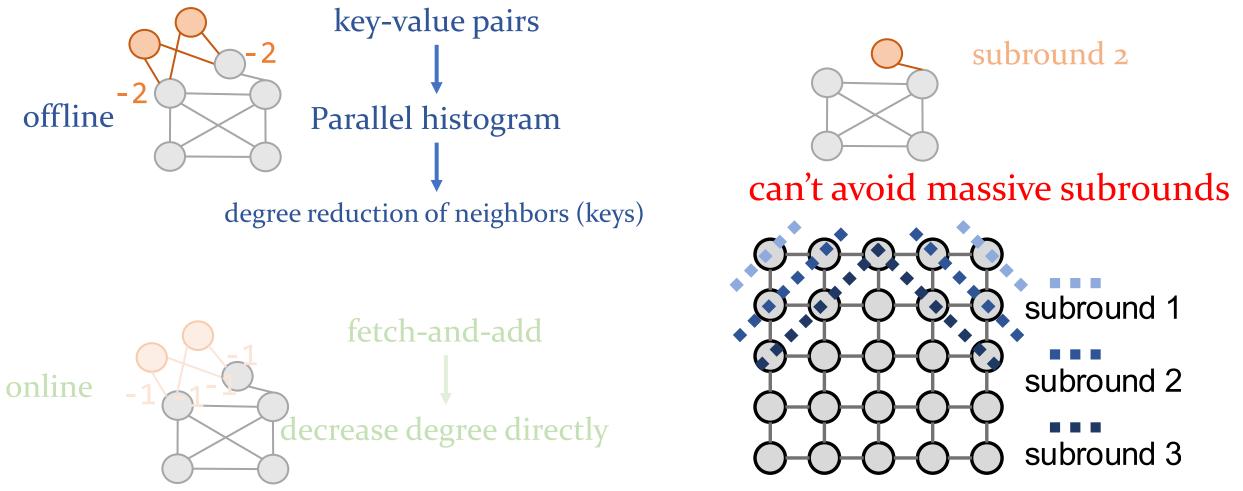
subround 1

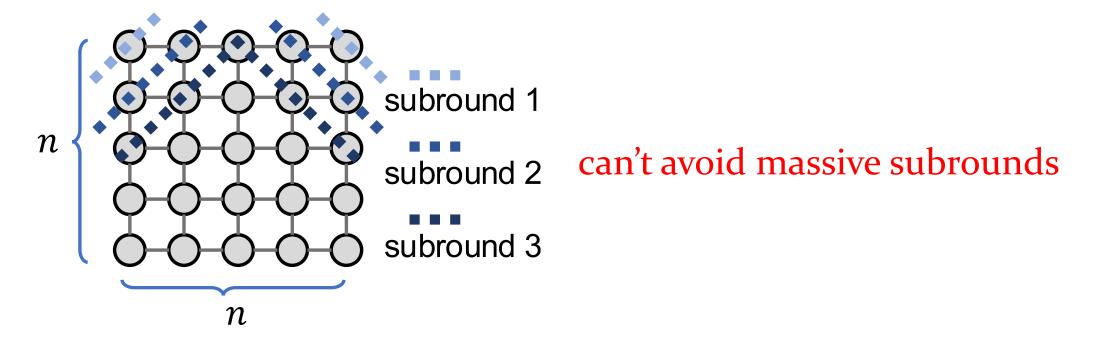




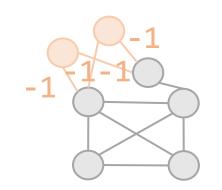
subround 2

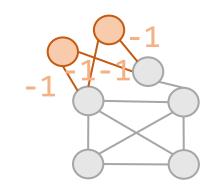




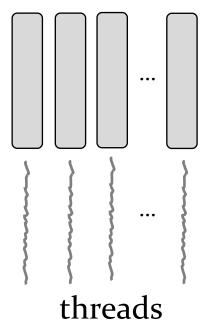


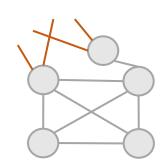
One subround = one round of synchronization overhead

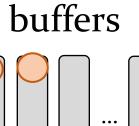


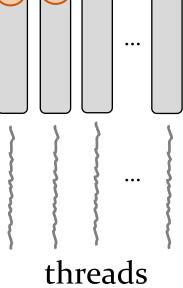


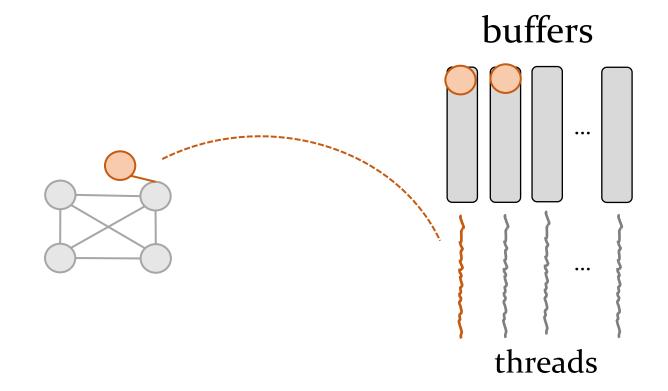


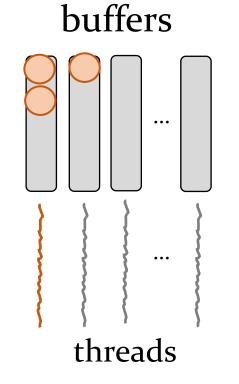


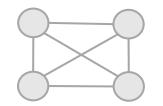


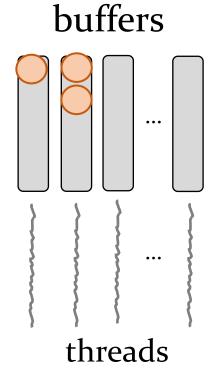


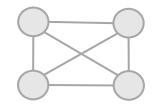


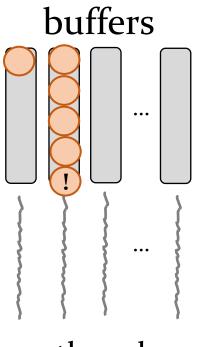








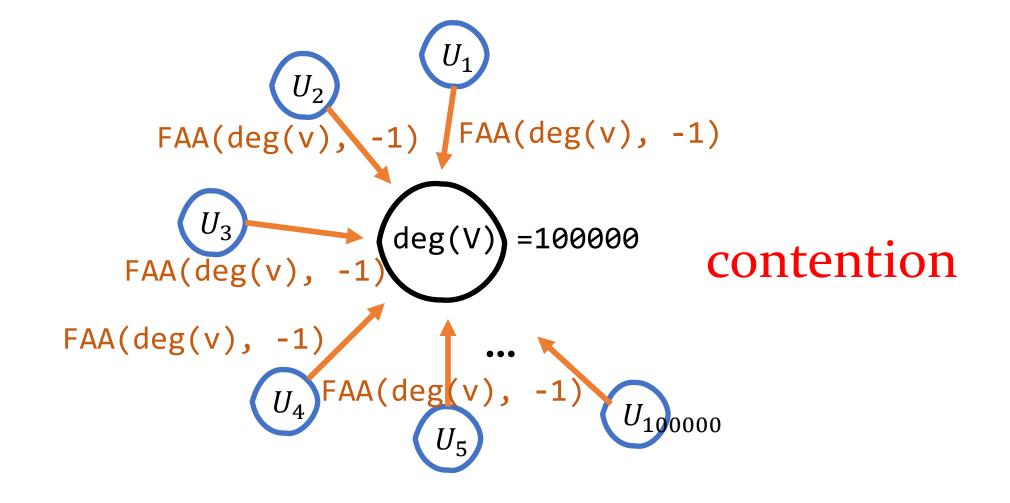




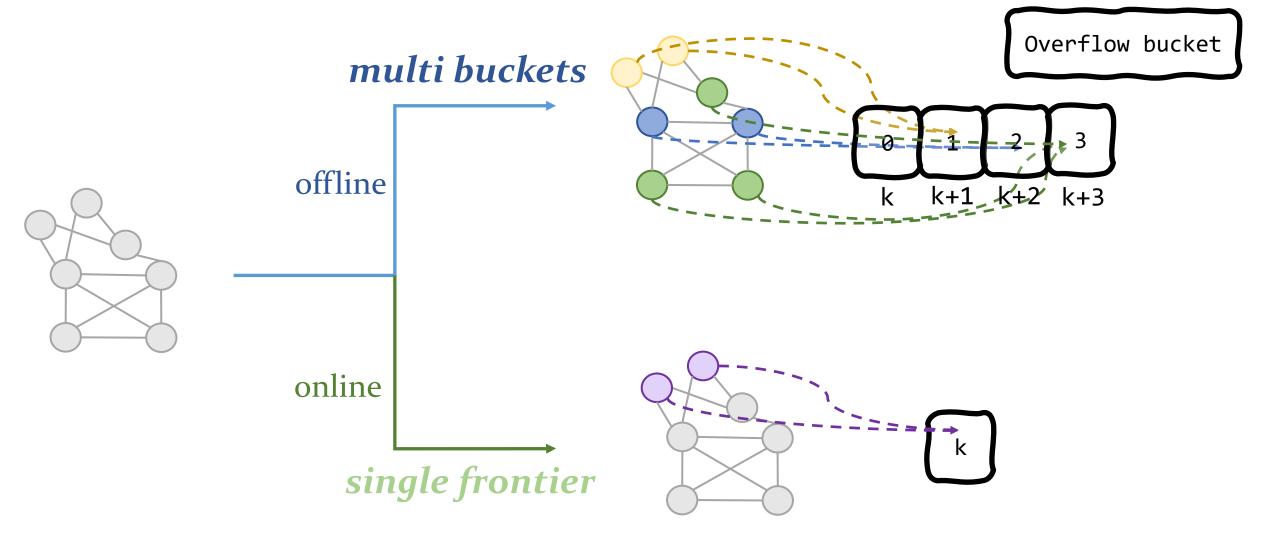
Issue from the subround reduction

Buffers may be full, and it hurts load balance

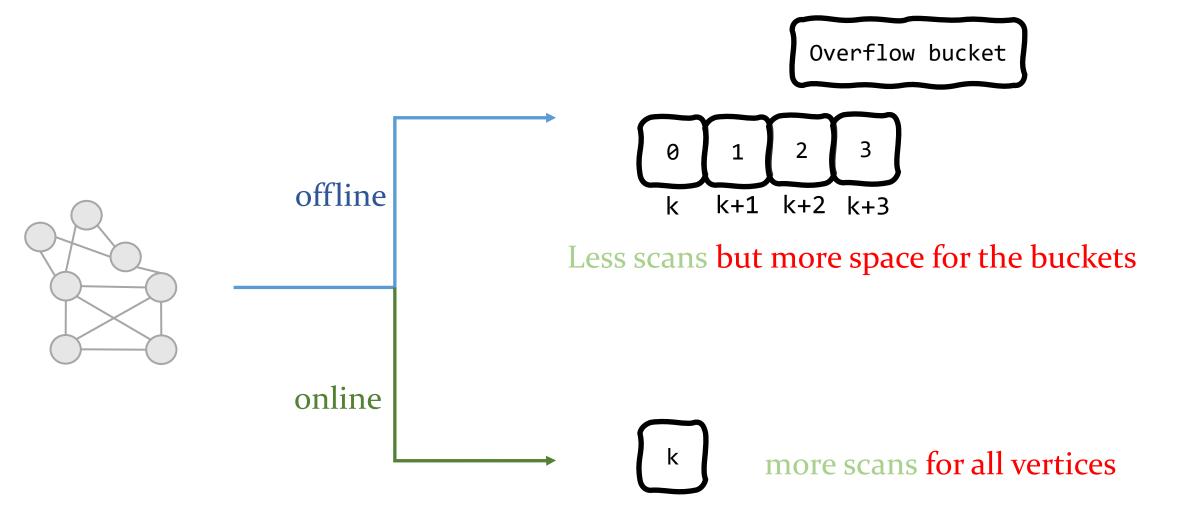
threads



The Second Challenge: How to arrange the vertices?



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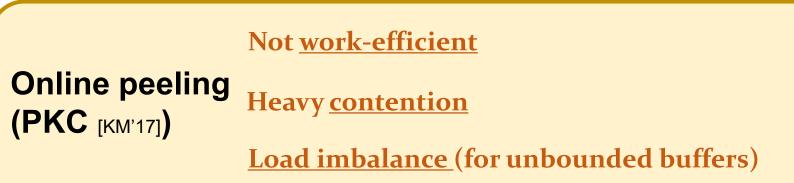
Summary of Existing Solutions

• Existing offline-peeling solutions

 Offline peeling
 Memory limitation for multi-bucket structure

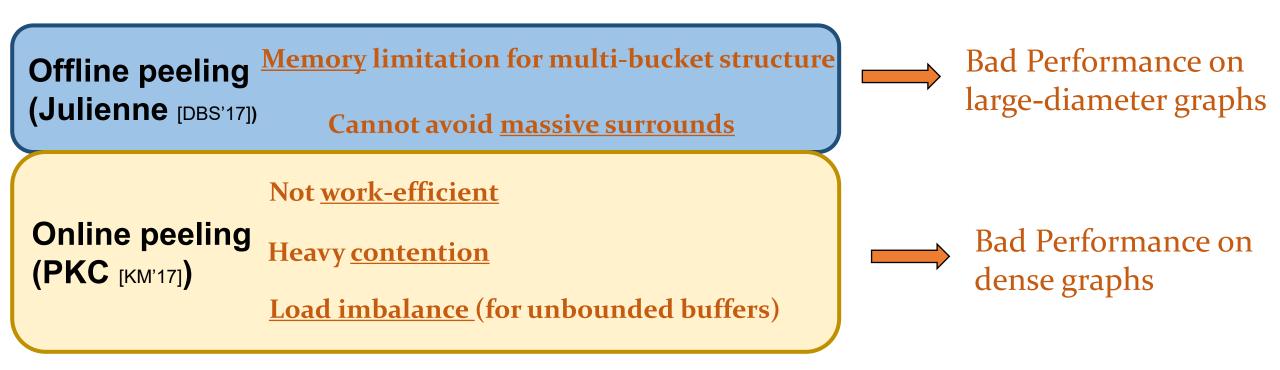
 (Julienne [DBS'17])
 Cannot avoid massive surrounds

• Existing online-peeling solutions



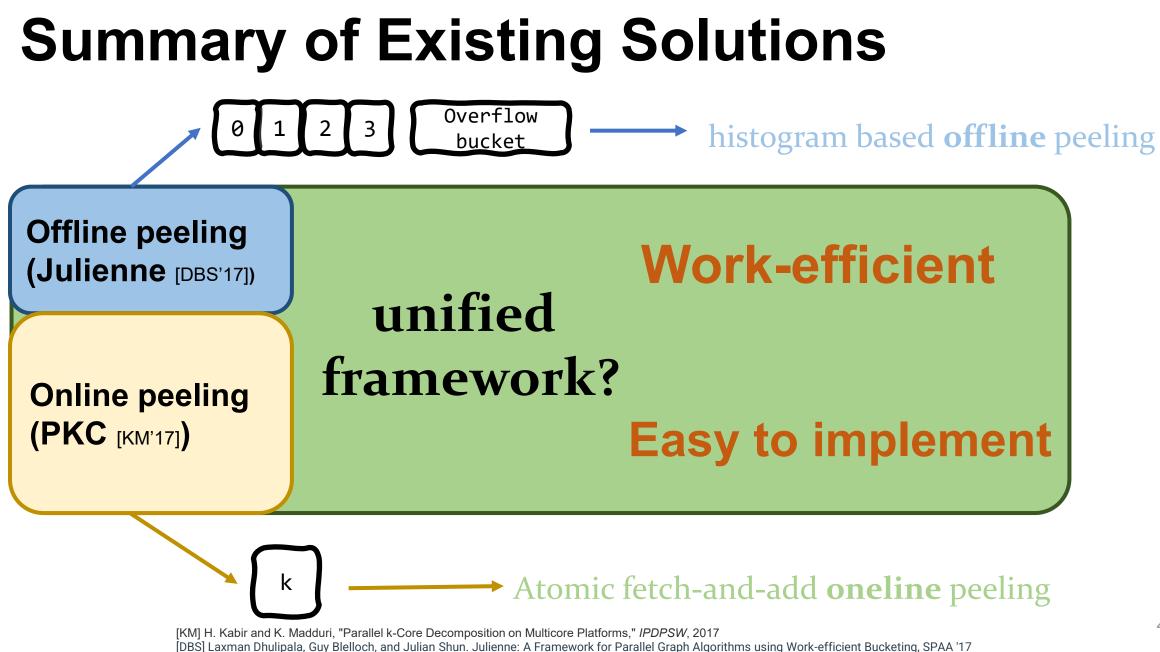
[KM] H. Kabir and K. Madduri, "Parallel k-Core Decomposition on Multicore Platforms," *IPDPSW*, 2017 [DBS] Laxman Dhulipala, Guy Blelloch, and Julian Shun. Julienne: A Framework for Parallel Graph Algorithms using Work-efficient Bucketing, SPAA '17

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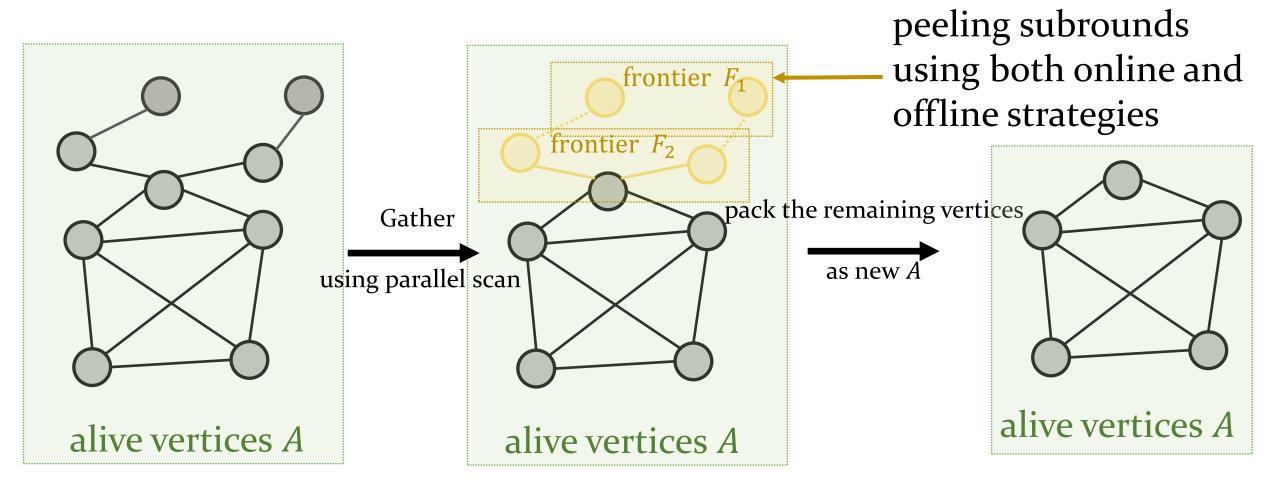


Can we get good performance on all types of graphs?

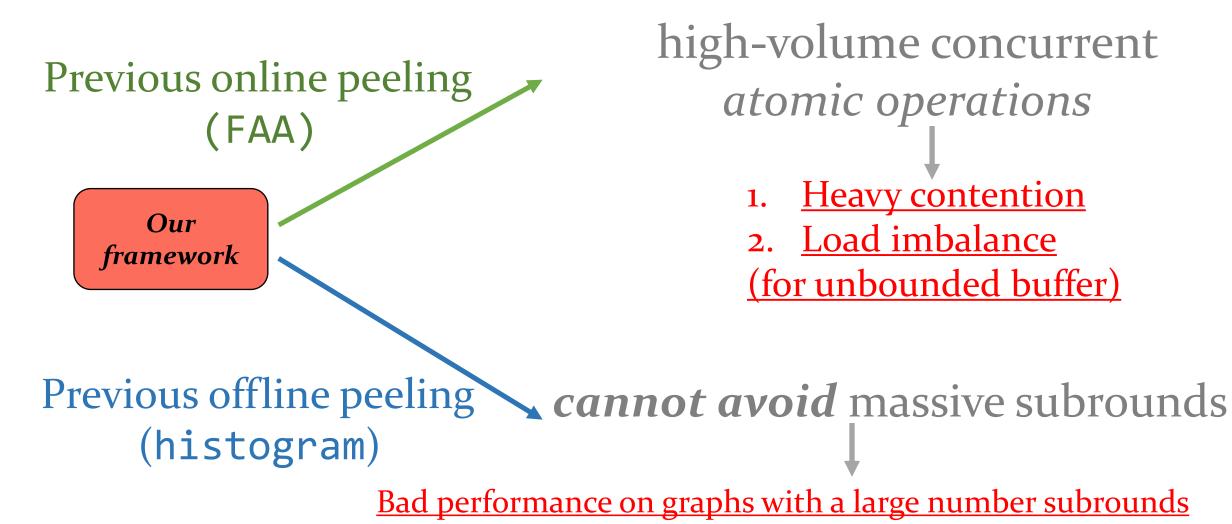
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Our *k*-core decomposition framework



Our Framework: adaptive for both peeling strategies

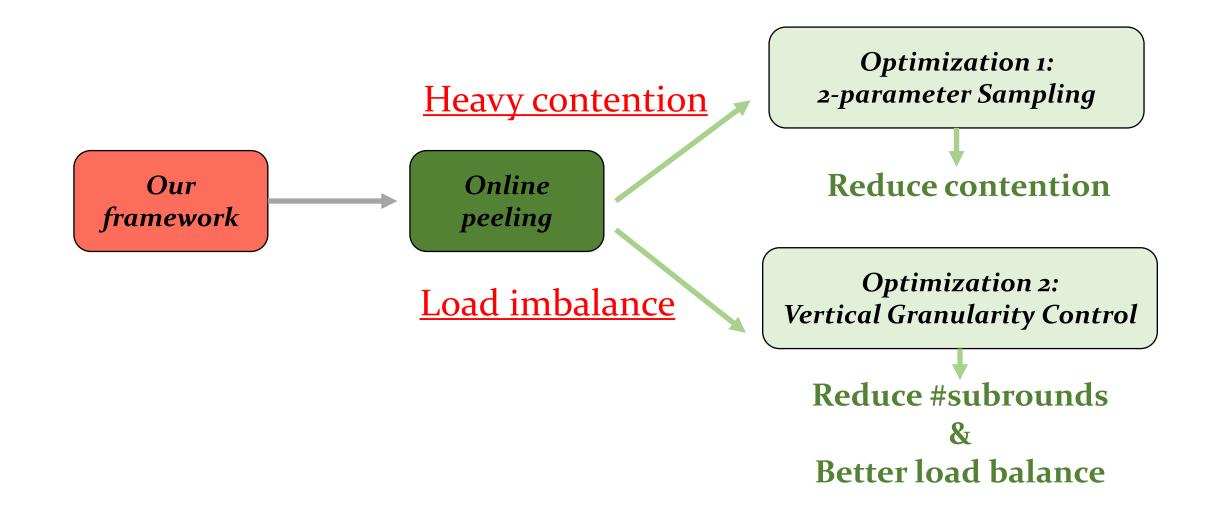


Our Algorithm: Optimized Online-peeling

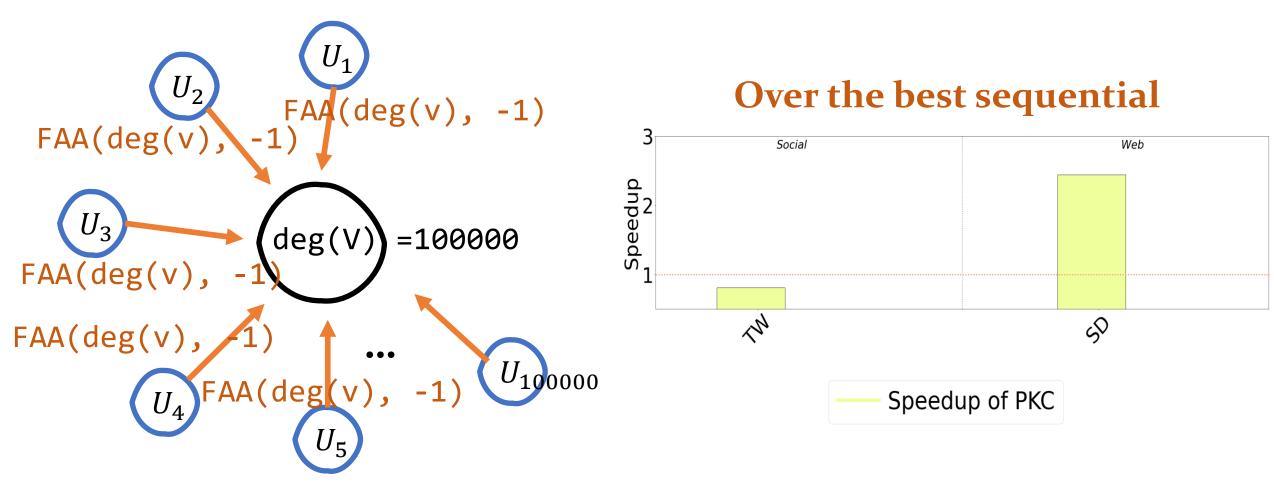


Inherited issue: a large number of subrounds

Our Algorithm: Optimized Online-peeling

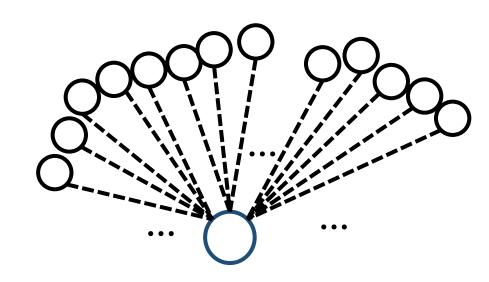


How can we reduce the contention?

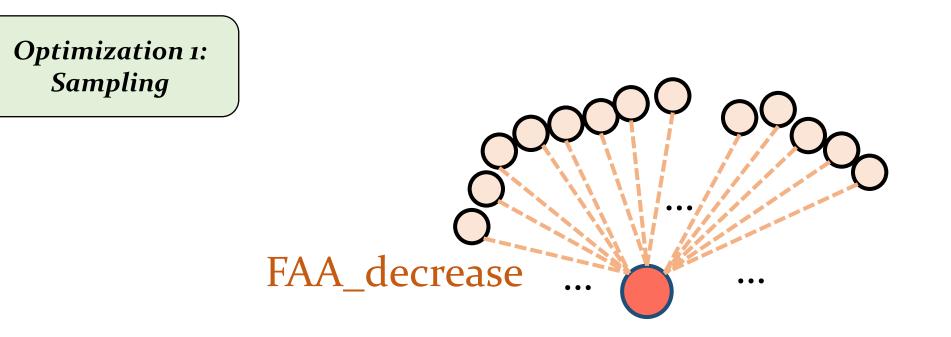


contention

Optimization 1: Sampling

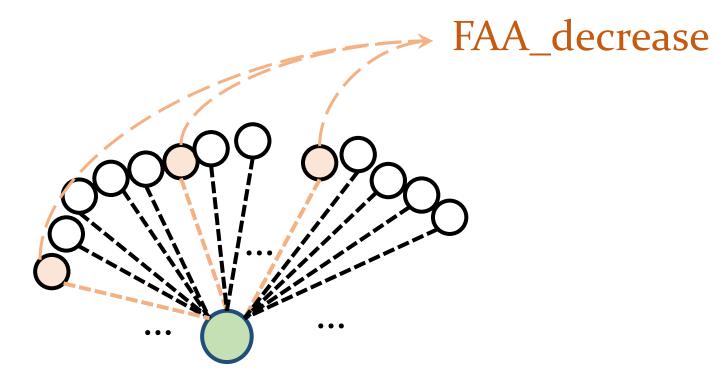


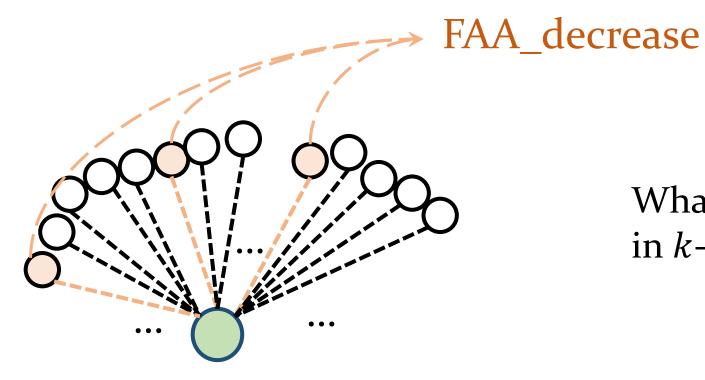
High-volume contention while FAA_dec



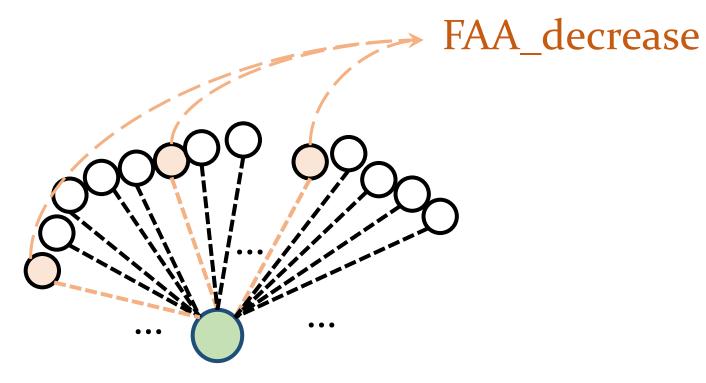
High-volume contention

Optimization 1: Sampling

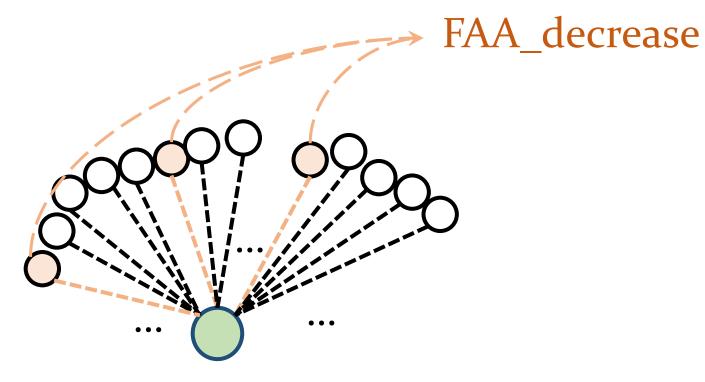


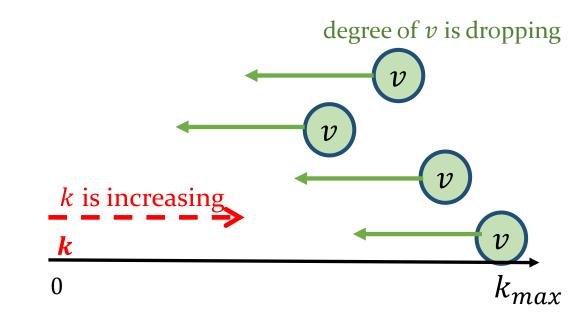


What is the difficulty of sampling in *k*-core decomposition?









Technique 1: 2-parameter Sampling

When will the error occur?

real neighbor deletions

58



counter value does not hit the threshold (still under sampling)

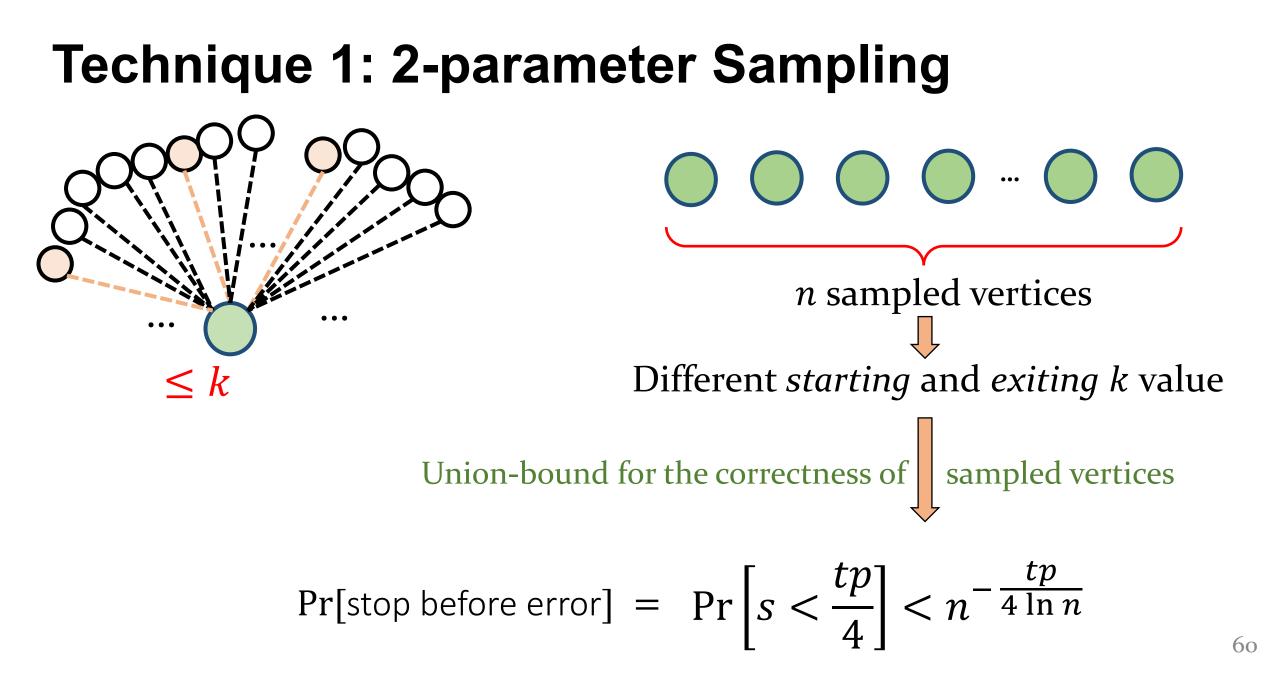
Technique 1: 2-parameter Sampling

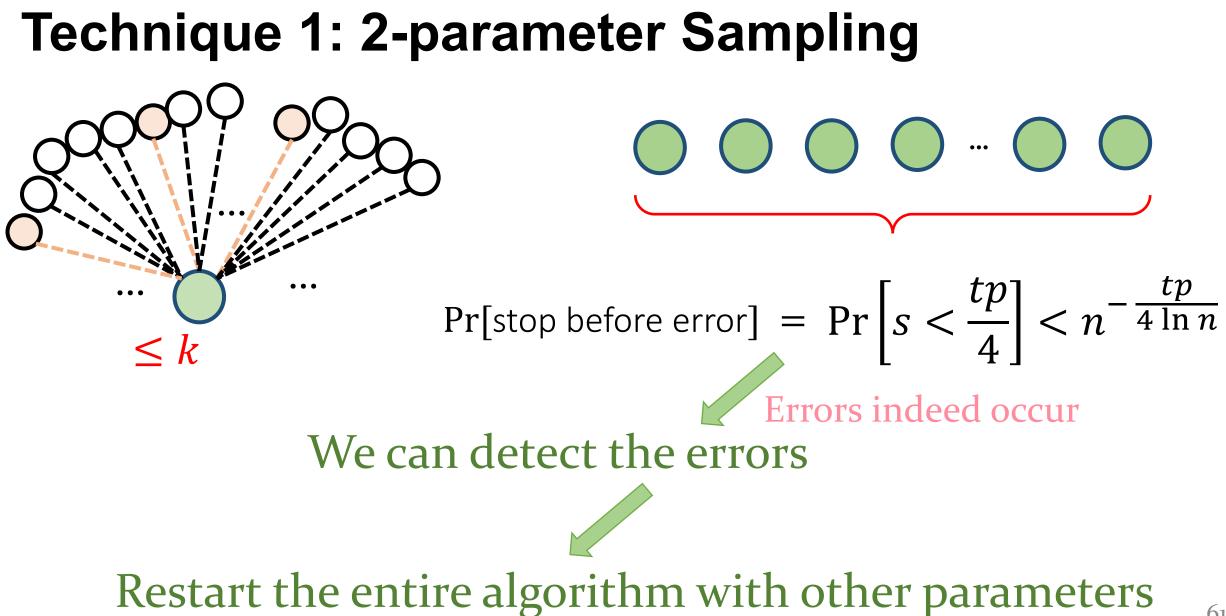
 $\leq K$

When the counter value is above a ratio w.r.t. *k*

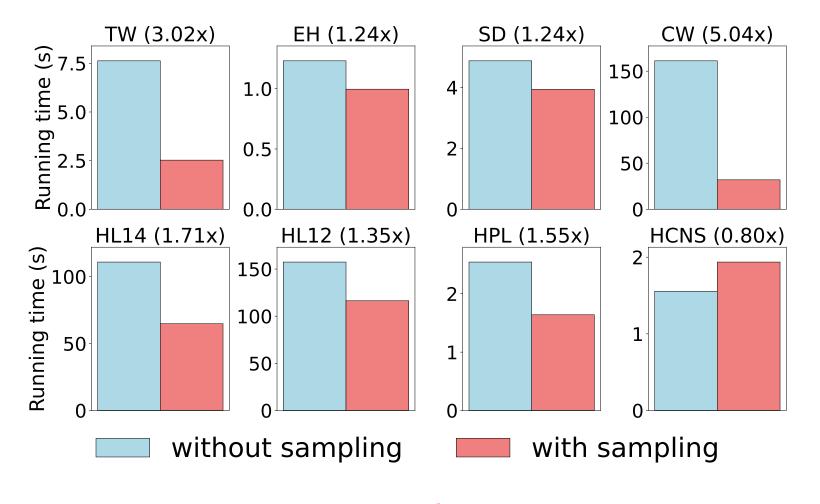
STOP sampling & Check the real induced degree (counting alive neighbors)

Can we make sure the sampling process is correct with high probability regarding |V| ?





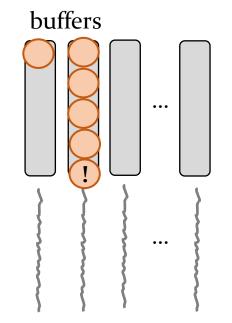
Performance Comparison for Sampling



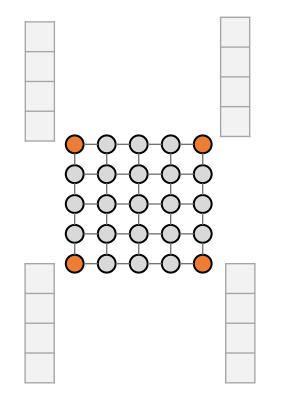
96-core machine (192 hyperthreads)

Lower is better

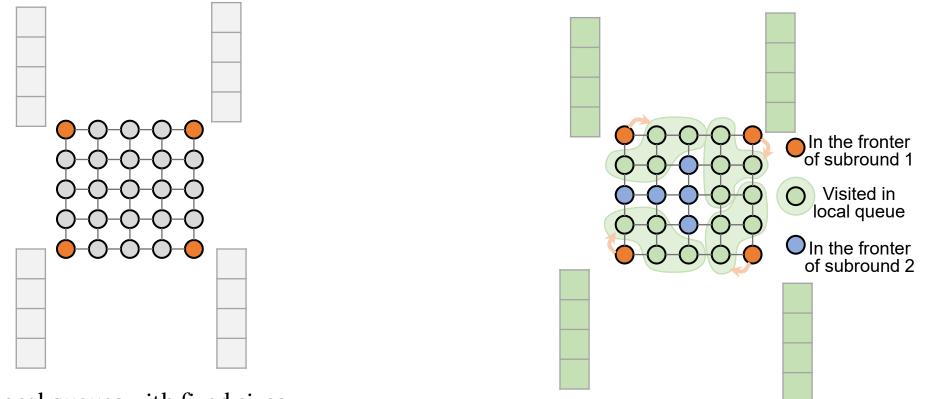
Reduce #Subround: A Better Design?



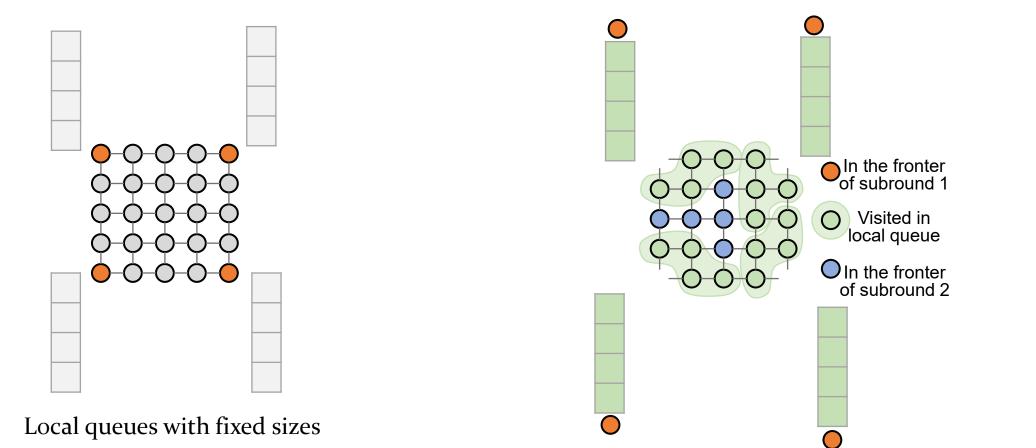
Unbounded buffers -> load imbalance & overflow

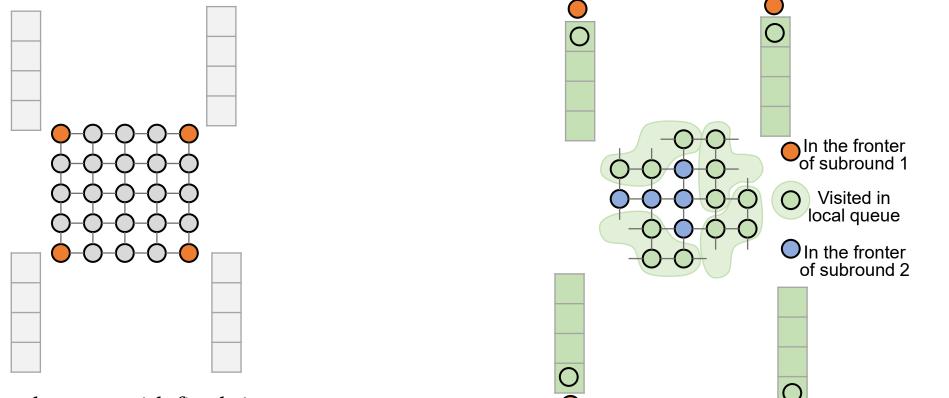


Local queues with fixed sizes

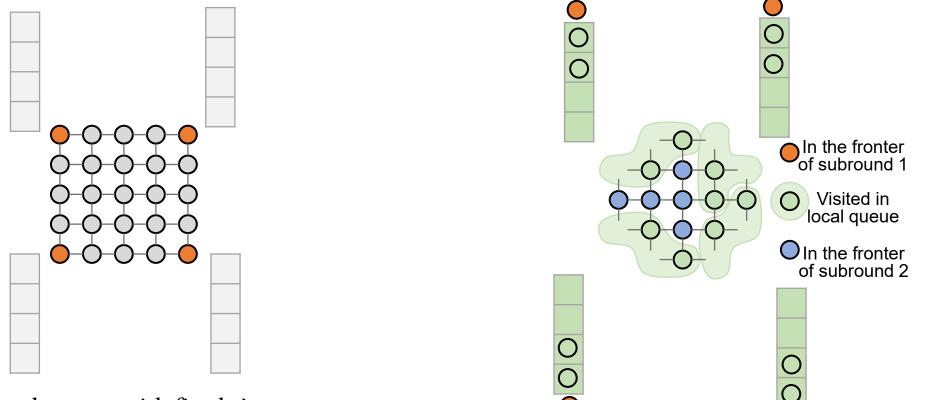


Local queues with fixed sizes

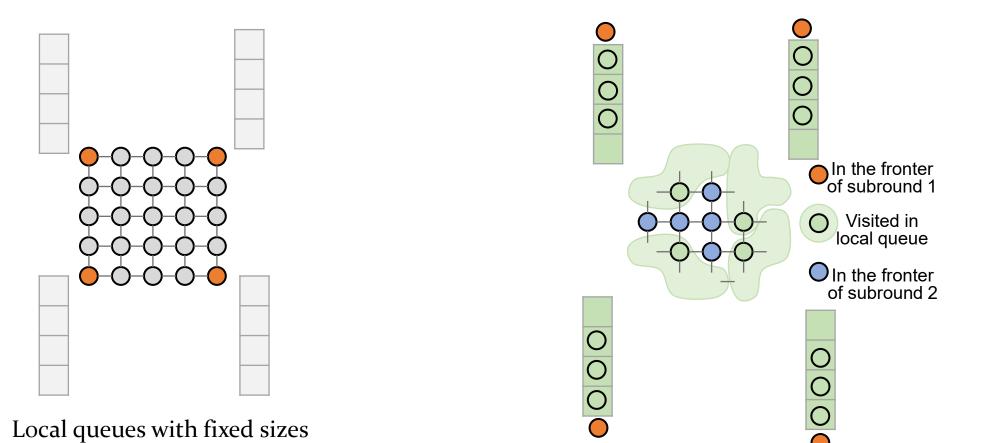


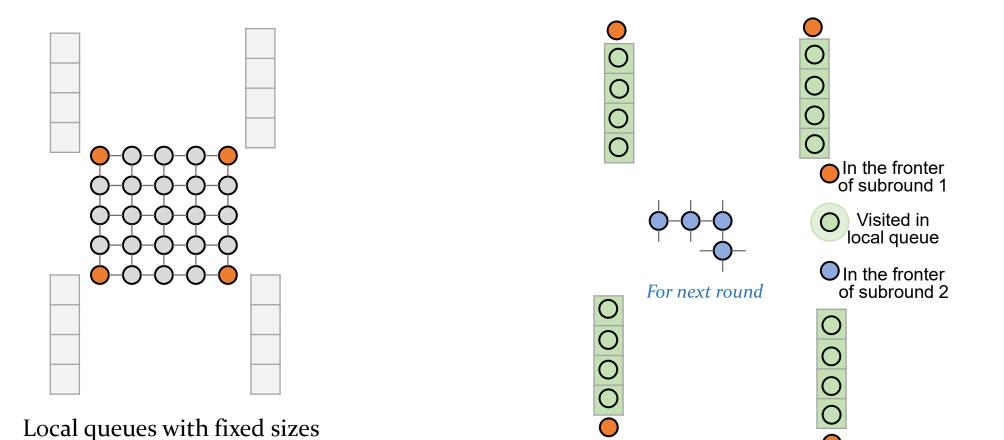


Local queues with fixed sizes

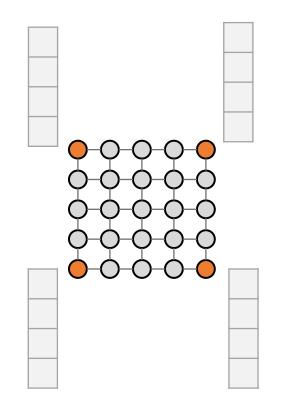


Local queues with fixed sizes

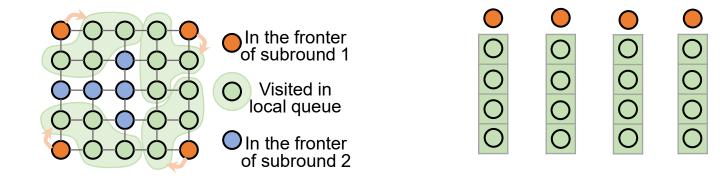




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Local queues with fixed sizes

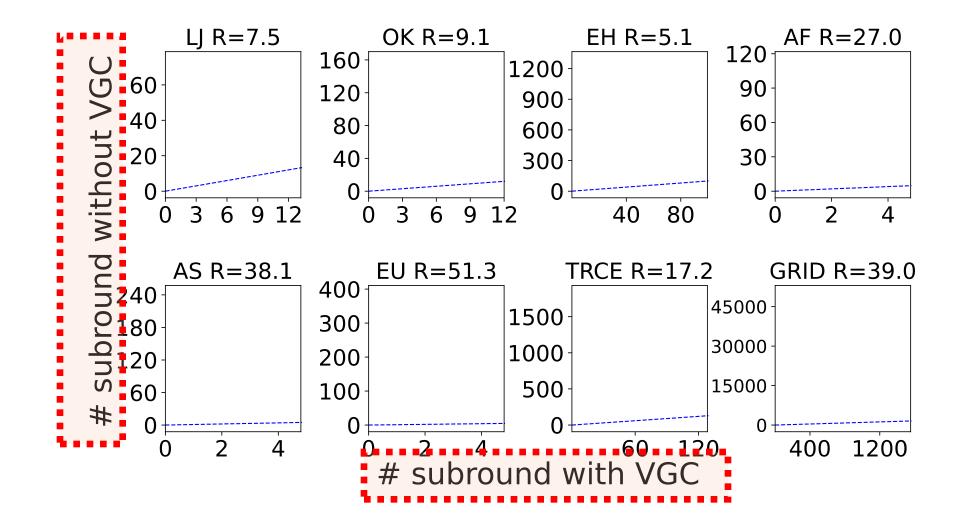


Reduce #subrounds (for a ratio)

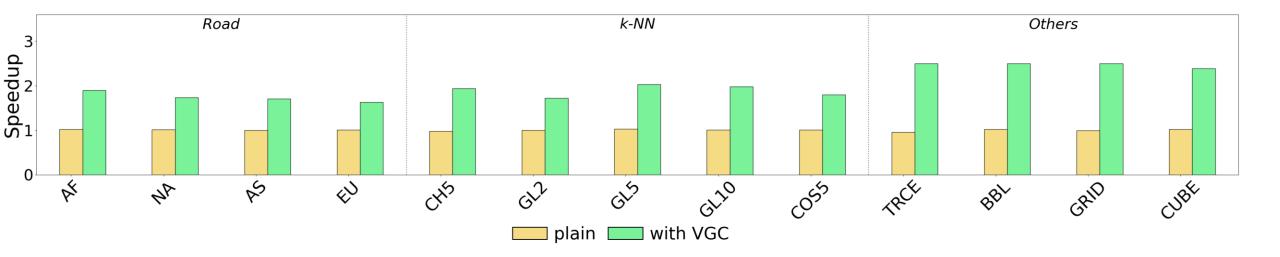
Load balance



Subround Reduction Ratio with VGC



VGC Provides Significant Speed-Up



Higher is better

Summary: Two Optimizations

Optimization 1: 2-parameter Sampling

Reduce contention

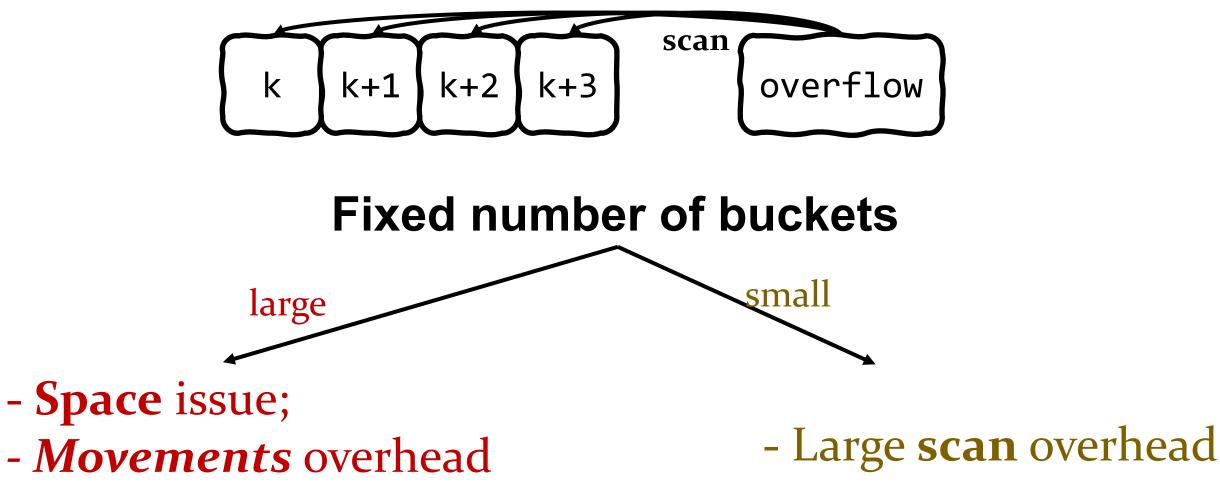
 Optimization 2:
 Reduce #subrounds

 Vertical Granularity Control
 &

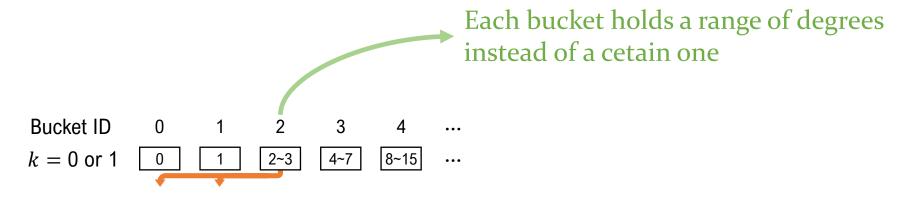
 Better load balance

Further improvements? Maintain vertices in a better structure?

Why do we need a better bucketing structure?

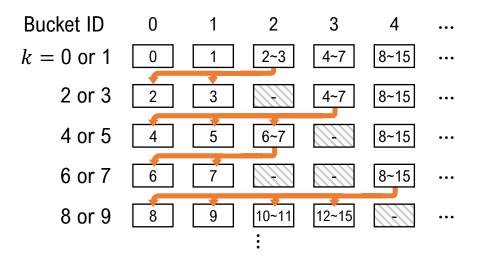


Our Hierarchical Bucketing Structure (HBS)



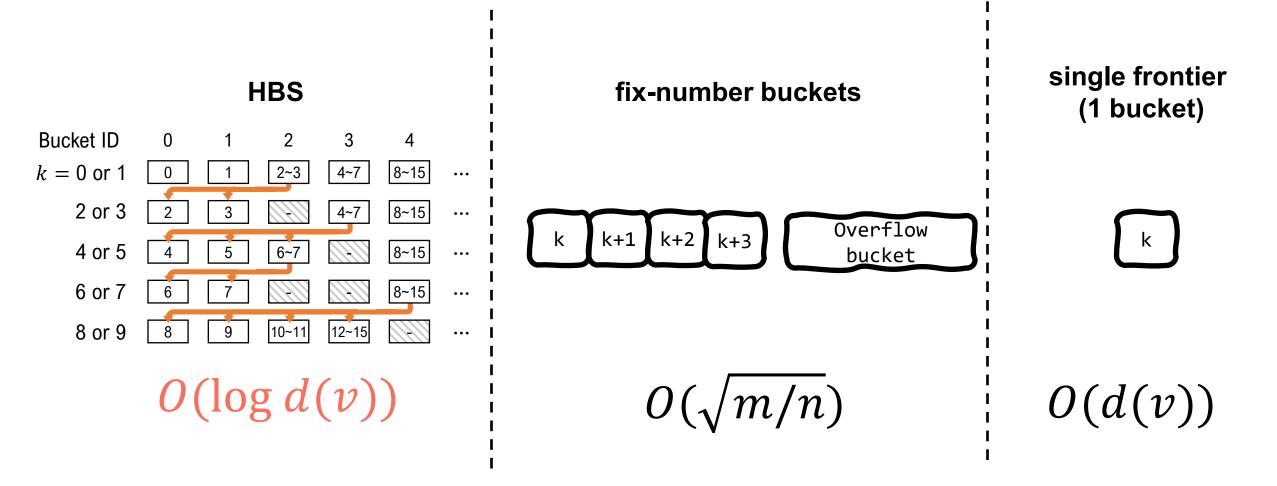
Bucketing structure in a **prefix-doubling** manner Range size 1, 1, 2, 4, 8, ...

Our Hierarchical Bucketing Structure (HBS)

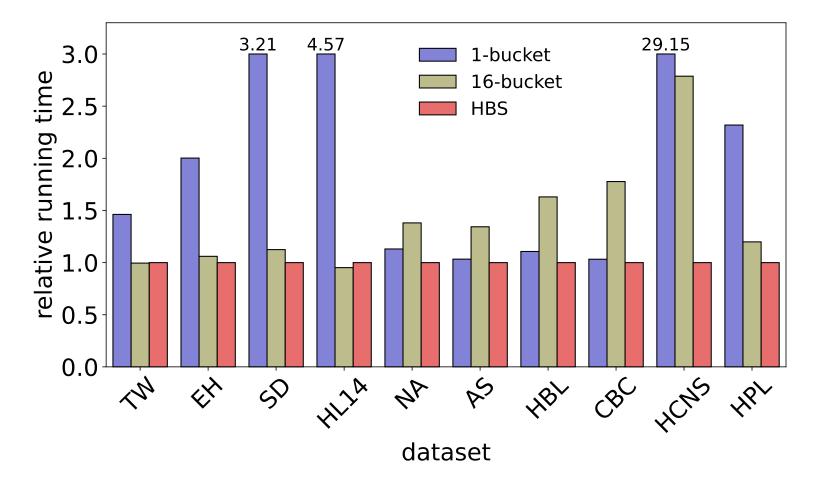


The new HBS can efficiently handle and balance all cases

Our Hierarchical Bucketing Structure (HBS)



HBS is overall better than the other two structures

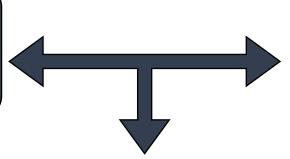


Lower is better

Experiments Setups

Experiments setup

 ParlayLib ^[1] for fork-join parallelism and primitives Complete tests on synthetic graphs

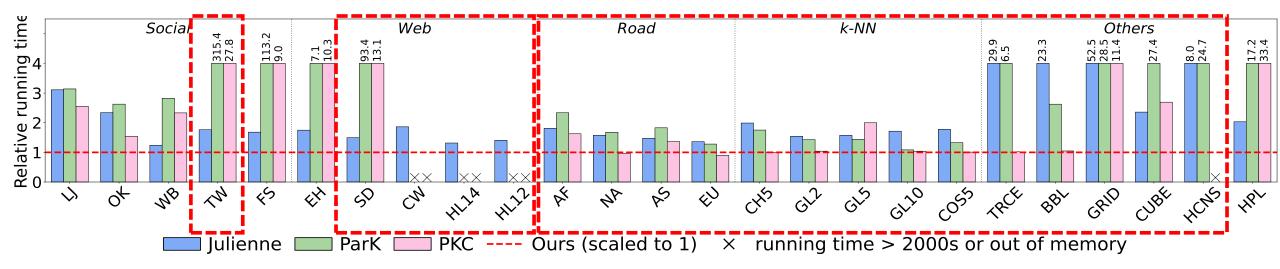


Large-scale real-world graphs

 96-core (192 hyperthreads) machine with four Intel Xeon Gold 6252 CPUs

Experiments on the performance of each proposed technique

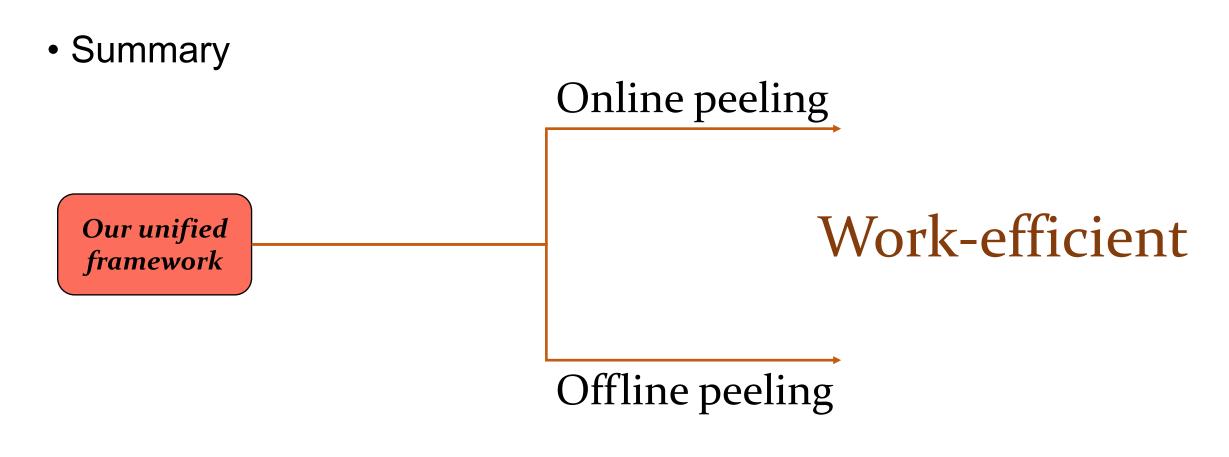
Our framework with all the techniques: Better Overall Performance



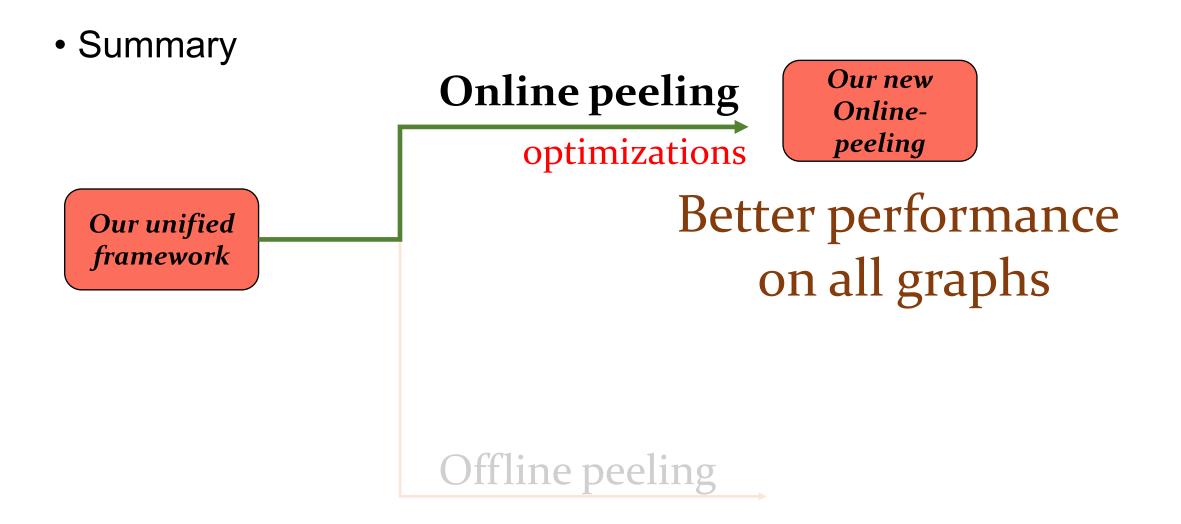
Lower is better

96-core machine
(192 hyperthreads)

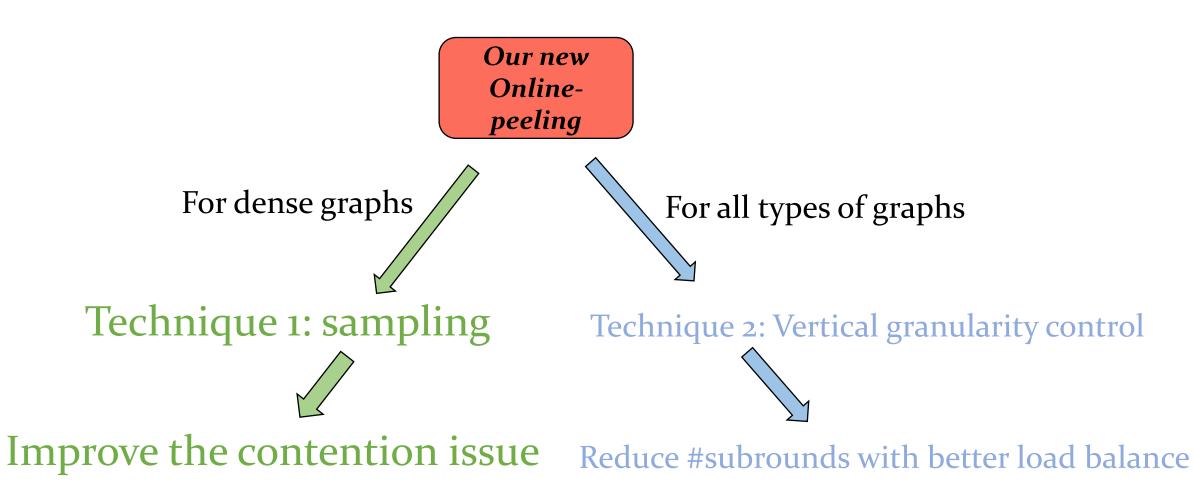
Conclusions: Work-efficient Framework



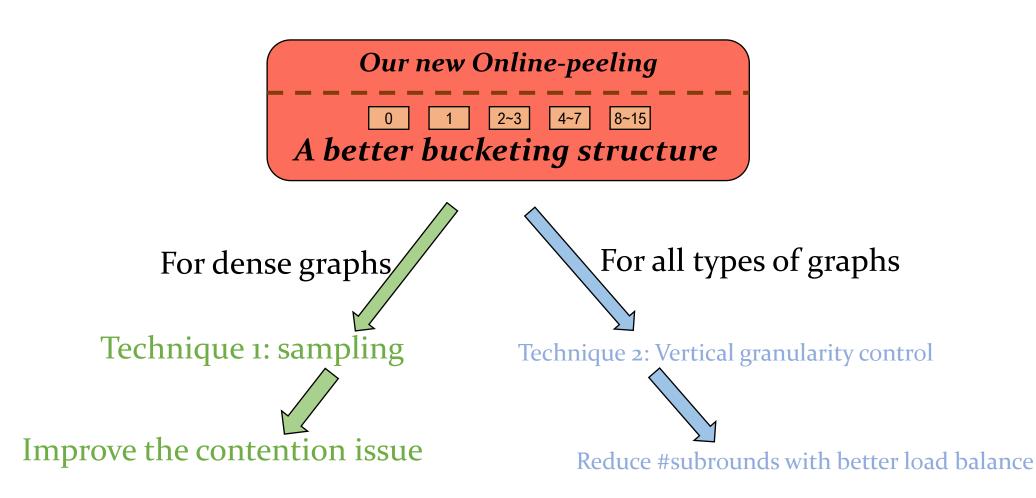
Conclusions: Improvements on Our Framework



Conclusions: Two Optimizations



Conclusions: HBS – A Better Structure



Contact

- Code on GitHub:
 - https://github.com/ucrparlay/PASGAL
- Contact
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